



RST Levels:
 P0.0=0,P3.0=0:12V
 P0.0=0,P3.0=1:Vih
 P0.0=1,P3.0=0: 0V
 P0.0=1,P3.0=1: 0V

All switches OFF means Verify only
 All ON means Erase>Prog>Protect>Lock

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To engage factory diag, place 0.1uF between pins 9&10 of AT89C2051 at power on, watch all lines with scope.



Title		PLCC Version
AT89C2051 Programmer		
Size	Number	Revision
A4	AB0810786	V1.02
Date: 17 Oct 2008	Sheet 1 of 1	
File: Z:\asrc\pg2051\pg2051_3	Drawn By: sm	

pg2051_3 pcb overlay

